

**Appl. No. 10/781,108**  
**Amdt. dated February 21, 2006**  
**Reply to Office action of November 25, 2005**

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently amended) A system comprising:  
a processor;  
a random access memory (RAM) coupled to the processor; and  
a bridge device coupling a system bus to the processor, the system bus configured to couple to an expansion bus of a managed computer system comprising a disk drive;  
wherein the RAM memory is configured to hold a bootable image for the managed computer system, and wherein the processor is configured to emulate a disk drive device storing the bootable image, and to boot the managed computer system from the bootable image stored in the ~~memory~~ RAM;;  
~~wherein the processor is configured to reboot the managed computer system without accessing a host processor of the managed computer system.~~
2. (Original) The system as defined in claim 1 wherein the processor is configured to determine a source of an error in the managed computer system by accessing components of the managed computer system over the system bus.
3. (Currently amended) The system as defined in claim 1 wherein the processor is configured to store information related to a state of the managed computer system in the ~~memory~~ RAM.
4. (Currently amended) The system as defined in claim 3 wherein the processor is configured to store diagnostic information about the managed computer system in the ~~memory~~ RAM.

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5. (Currently amended) The system as defined in claim 3 wherein the processor is configured to store event log information about the managed computer system in the ~~memory~~RAM.

6. (Canceled).

7. (Currently amended) The system as defined in claim 1 further comprising:  
a network interface coupled to the ~~memory~~RAM by the system bus;  
wherein the bootable image is transferable to the ~~memory~~RAM through the network interface.

8. (Currently amended) The system as defined in claim 7 wherein the bootable image is transferable to the ~~memory~~RAM through the network interface from a remote management console.

9. (Currently amended) The system as defined in claim 7 wherein the bootable image is transferable to the ~~memory~~RAM through the network interface using file transfer protocol (FTP) software.

10. (Currently amended) The system as defined in claim 7 wherein the bootable image is transferable to the ~~memory~~RAM through the network interface using TFTP software.

11. (Currently amended) A method comprising:  
transferring a bootable image for a managed computer system to a  
memory ~~within~~of a management sub-system ~~within~~coupled to the  
managed computer system; and  
emulating a floppy drive by the management sub-system to boot the  
managed computer system from the bootable image in the memory  
of the management sub-system; and

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~~configuring a processor of the management sub-system to reboot the managed computer system without accessing a host processor of the managed computer system.~~

12. (Original) The method as defined in claim 11 further comprising transferring the bootable image to the memory at the behest of a remote management console.

13. (Original) The method as defined in claim 12 further comprising transferring the bootable image to the memory from the remote management console.

14. (Original) The method as defined in claim 12 further comprising transferring the bootable image through a network interface coupled to the management sub-system using a File Transfer Protocol (FTP) software.

15. (Original) The method as defined in claim 12 further comprising transferring the bootable image through a network interface coupled to the management sub-system using TFTP software.

16. (Previously presented) The method as defined in claim 11 further comprising rebooting the managed computer system, by the processor of the management sub-system, prior to emulating.

17. (Currently amended) A management sub-system comprising:  
a processor;  
a memory coupled to the processor, the memory storing a bootable image for a managed computer system comprising a disk drive and an expansion bus; and

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a bridge device coupling a system bus to the processor, and wherein the system bus is configured to couple to an the expansion bus of the managed computer system;

wherein the processor is configured to emulate a disk drive device storing the bootable image, and the processor is further configured to boot the managed computer system from the bootable image stored in the memory;

~~wherein the processor is configured to reboot the managed computer system without accessing a host processor of the managed computer system.~~

18. (Original) The management sub-system as defined in claim 17 further comprising:

a network interface coupled to the memory by the system bus;

wherein the bootable image is transferable to the memory through the network interface.

19. (Original) The management sub-system as defined in claim 18 wherein the bootable image is transferable to the memory through the network interface from a remote management console.

20. (Original) The management sub-system as defined in claim 18 wherein the bootable image is transferred to the memory through the network interface using TFTP software.

21. (Original) The management sub-system as defined in claim 17 wherein the processor, memory and network interface are mounted on an add-in card configured to be substantially within the chassis of the managed computer system.

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22. (Original) The management sub-system as defined in claim 17 wherein the system bus is a Peripheral Components Interconnect (PCI) bus.

23. (New) The system as defined in claim 1 wherein the processor is configured to reboot the managed computer system without accessing a host processor of the managed computer system.

24. (New) The system as defined in claim 1 wherein the system bus and the expansion bus of the managed computer system utilize the same bus protocol.

25. (New) The system as defined in claim 24 wherein the bus protocol is a Peripheral Component Interconnect (PCI) bus.